



Advances in Analog and RF IC Design for Wireless Communication Systems: Chapter 8. Bandpass $\Delta\Sigma$ ADCs for Wireless Receivers

Richard Schreier, Hajime Shibata

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Design concepts and an example of bandpass Delta-Sigma ADCs for wireless receivers are described. Delta-Sigma ADC design involves a variety of choices in the system design phase, such as lowpass vs. bandpass loop filter or, feedback vs. feedforward architecture. In the first half of the chapter, such system decisions are compared and discussed in the context of wireless communication applications. The latter half of the Chapter describes an example bandpass Delta-Sigma ADC design based on the aforementioned discussions. Circuit implementations and measurement results are disclosed in details.

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